| Form PTO 1449 U.S. DEPARTMENT OF COMMERCE (Modified) PATENT AND TRADEMARK OFFICE |               |   |  | ATTY DOCKET NO.   |  | SERIAL NO.                                       |  |  |
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|  |               |   |  | 240471US2S  | NEW APPLICATION                                  |  |  |  |
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| LIST OF  | KEFEF         | RENCES CITED BY AP  | PLICANI  | Kenji TSUCHIDA, et al.  |  |  |  |  |
|  |               |   |  | FILING DATE<br>HEREWITH   |  | GROUP  |  |  |
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| EXAMINER<br>INITIAL  |               | DOCUMENT<br>NUMBER  | DATE   | NAME  | CLASS  | SUB<br>CLASS                                     | CLASS IF APPROPRIATE                             |  |
| MT   | AA            | 6,259,644 B1  | 07/10/01   | Lung T. TRAN, et al.  | 365  | 209  |  |  |
|  | AB            |   |  |   |  |  |  |  |
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|  |               |   |  | Including Author, Title, Date, Pertinen   |  | •  |  |  |
| ME   | AW            | Roy SCHEUERLEIN, et al., "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in each Cell", ISSCC 2000 Digest of Technical Paper, 2000, pgs. 128-129 |  |   |  |  |  |  |
|  | AX            | ,   |  |   |  |  | <del>*************************************</del> |  |
|  | AY            |   |  |   |  |  |  |  |
|  | AZ            | Additional References sheet(s) atta   |  |   |  |  |  | neet(s) attached                               |
| Examiner M. TRAN   |               |   |  | Date Co   | Date Considered 8/9/04                           |  |  |  |
| conformance  | and no        | eference is considered<br>it considered. Include c  | , whether or no<br>opy of this form              | t citation is in conformance with MPEP 6<br>n with next communication to applicant. | 09; <b>Draw</b> I                                | ne through                                       | citation if                                      | not in   |